

- Drafts
- BRS:
- BRS: 24 and
- Pending
- Active
 - L1: (57539) refresh\$3
 - L2: (2336886) block or blocks
 - L3: (593791) segment or segments
 - L4: (4356) sub adj (array or arrays)
 - L5: (3613) subarray or subarrays
 - L6: (554902) register or registers
 - L7: (3401) 1 near8 (2 or 3 or 4 or 5)
 - L8: (272) 7 with 6
 - L11: (21526) ("711").CLAS.
 - L12: (61711) ("365").CLAS.
 - L14: (142) 8 and (11 or 12)
 - L15: (85137) dram
 - L16: (57695) dynamic adj4 memory
 - L17: (116) 14 and (15 or 16)
 - L18: (344113) reset
 - L19: (819426) inhibit\$3
 - L20: (81784) halt or halting or halted
 - L21: (1054364) access or accessed
 - L22: (11) 1 with (2 or 3) with (19 or
 - L23: (13) 8 same "28"
 - L24: (27) 8 same 18
 - L25: (17) 24 and (15 or 16)
- Failed
- Saved
- Favorites
- Tagged (1)

Browse Queue

DBs USPA Plurals

Default operator: Highlight all hit terms initially

24 and (15 or 15)

*Fig 1
365/222
149
230.03*

BRSI Book Image Text HTML

U	I	Document ID	Issue Date	Pages	Title	Current OR	Currt
1	<input type="checkbox"/>	US 20040047	20040311	17	DRAM with refresh contr	365/202	
2	<input type="checkbox"/>	US 20030067	20030410	68	Semiconductor memory de	365/222	
3	<input type="checkbox"/>	US 20030028	20030206	12	Monitoring in-use memor	711/106	
4	<input type="checkbox"/>	US 20020056	20020509	38	Read/write buffers for	711/106	711/1
5	<input type="checkbox"/>	US 20010007	20010712	32	Single-Port multi-bank	365/222	
6	<input type="checkbox"/>	US 6616911	20031111	65	Semiconductor memory de	365/222	365/1

HS Details HTML

Ready NUM